



JPW

Docket No.: M4065.0741/P741
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Kit Fai Chan et al.

Application No.: 10/733,474

Confirmation No.: 8825

Filed: December 12, 2003

Art Unit: 2171

For: CURRENT MODE LOGIC SCHEME AND
CIRCUIT FOR MATCH LINE SENSE
AMPLIFIER DESIGN USING CONSTANT
CURRENT BIAS CASCODE CURRENT
MIRRORS

Examiner: Not Yet Assigned

STATUS INQUIRY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

It is respectfully requested that the attorney named below be advised of the status of the above-identified application. Please advise us of when we might expect to receive an Office Action from the Patent and Trademark Office.

Dated: January 13, 2005

Respectfully submitted,

By 

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